

Roll No.

24165

**B. Tech. 5th Semester (ECE)
Examination – February, 2022
COMPUTER ARCHITECTURE AND ORGANIZATION
Paper : CSE-210-P**

Time : Three Hours]

[Maximum Marks : 100

Before answering the questions, candidates should ensure that they have been supplied the correct and complete question paper. No complaint in this regard, will be entertained after examination.

Note : Attempt five questions in all, selecting one question from each Unit. Question No. 1 is compulsory. All questions carry equal marks

1. Write short notes on the following : 20
- (a) MIPS
 - (b) RISC
 - (c) Cache memory
 - (d) Stack

24165-202(P-3)/(O-9)/(22)

P. T. O.

UNIT - I

2. (a) What is the difference between combinational logic and sequential blocks ?
10
(b) Define various types of memory. 10
3. (a) Define Flynn's classification of computers. 10
(b) Explain structured organization in detail. 10

UNIT - II

4. (a) Compare RISC and CISC in detail. 10
(b) Explain addressing modes of the computer. 10
5. Define various types of instruction. 20

UNIT - III

6. (a) What is the need for a memory Hierarchy ? 10
(b) Explain CPU architecture types. 10
7. (a) Compare associative and direct mapped cache. 10
(b) What is micro instruction sequencing ? Explain. 10

24165- (P-3)(O-9)(22)

(2)

UNIT - IV

8. (a) What is Amdahl's Law ? Explain. 10
(b) Explain Instruction Level Parallelism. 10
9. (a) Define various types of Interrupt. 10
(b) Describe control memory in detail. 10

24165- (P-3)(O-9)(22)

(3)